

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Koichi Tamura

Examiner: Unassigned

Serial No.: Unassigned

Art Unit:

Filed: **Herewith**

Docket: 13392

For: DEMODULATION AND MODULATION
CIRCUIT AND DEMODULATION AND
MODULATION METHOD

Assistant Commissioner for Patents
Washington, D.C. 20231

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INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Japanese Unexamined-Patent Publication No. 8-223132, dated August 30, 1996;
2. Japanese Unexamined-Patent Publication No. 10-260653, dated September 29, 1998;
3. Japanese Patent No. 2570126 dated October 24, 1996 (Publication No. 7-50623).

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Dated: February 23, 2000

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Applicants are submitting copies of the above-cited references together with English language abstracts thereof. The relevance of the references has been described in the specification. Therefore, translations are not required.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no petition, certification or fee is required.

Respectfully submitted,



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